

WHAT IS CLAIMED IS:

1. A device comprising:

a receiving shift register for inputting serial data
synchronously with a communication clock signal received along
5 with the serial data;

a reception completion determination means for
outputting a reception completion signal when detecting that
the receiving shift register has input a predetermined number
of bits of the serial data;

10 a pulse-signal generation means for generating a timing
pulse signal, which comprises only a predetermined number of
timing pulses required for processing the input serial data,
in accordance with generation of the reception completion
signal by the reception completion determination means; and

15 a data processing means for processing the input serial
data synchronously with the timing pulse signal.

2. A device according to claim 1, wherein the reception
completion determination means includes a counter for counting
20 the number of input pulses included in the communication clock
signal, and for outputting the reception completion signal when
a count value of the counter reaches a predetermined number.

3. A device according to claim 1, wherein the reception
25 completion determination means monitors the serial data for a
special bit added to the serial data and outputs the reception
completion signal when the special bit is detected.

4. A device according to claim 1, further comprising:
an oscillation unit for generating a system clock signal
on the basis of an oscillation command signal;
5 an oscillation control unit for outputting the
oscillation command signal; and
a control logic unit operating synchronously with the
system clock signal.

10 5. A device according to claim 4, wherein the oscillation
control unit outputs the oscillation command signal on the basis
of command data.

6. A device according to claim 2, further comprising:
15 an oscillation unit for generating a system clock signal
on the basis of an oscillation command signal;
an oscillation control unit for outputting the
oscillation command signal; and
a control logic unit operating synchronously with the
20 system clock signal,

wherein the oscillation control unit outputs the
oscillation command signal functioning as an oscillation start
signal when the counter serving as the reception completion
means has counted a predetermined number of pulses included in
25 the communication clock signal.

7. A device according to claim 4, further comprising:

a timer,

wherein the oscillation control unit outputs the oscillation command signal functioning as an oscillation end signal when the timer has measured time of a predetermined length.